**DESIGN AND COMPARISION OF ASSORTED FACTS CONTROLLERS IN POWER SYSTEMS**

A PROJECT REPORT

**Submitted in partial fulfillment of the Requirement for the award of the degree of**

**BACHELOR OF TECHNOLOGY**

**IN**

**ELECTRICAL AND ELECTRONICS ENGINEERING**

**Submitted by**

**A.GOPI (11F05A0201)**

**Y.ANAND BABU (10F01A02B9) U.DURGA RAO (10F01A02B0)**

**R.YEDUKONDALU NAIK (11F05A0221)**

**Under the esteemed guidance of**

**Mr.B.ASHOK KUMAR M.E (Ph.D)**

**Assistant Professor**



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